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	Application No.	Applicant(s)
Madica at Allian at 1994	09/895,185	ITOH, NAOTSUGU
Notice of Allowability	Examiner	Art Unit
	Jerome Grant II	2626
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED i) or other appropriate comm (IGHTS. This application is	n this application. If not included unication will be mailed in due course. THIS
1. This communication is responsive to 4/22/2005.		
2. ☑ The allowed claim(s) is/are <u>1-13</u> .		
3. \boxtimes The drawings filed on <u>02 July 2001</u> are accepted by the Ex	xaminer.	
 4. Acknowledgment is made of a claim for foreign priority unas a) All b) Some* c) None of the: Certified copies of the priority documents have Certified copies of the priority documents have Copies of the certified copies of the priority documents have an accordance of the priority documents have an accordance of the priority documents have an accordance of the priority documents have a copies of the certified copies of the priority documents have a copies of the certified copies of the priority documents have a copies of the certified copies of the priority documents have a copies of the certified copies of the priority documents have a copies of the certified copies of the priority documents have a copies of the priority documents. 	e been received. e been received in Applicati	on No
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		e a reply complying with the requirements
5. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give		
6. CORRECTED DRAWINGS (as "replacement sheets") mus	st be submitted.	
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached		
1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examiner' Paper No./Mail Date	s Amendment / Comment o	r in the Office action of
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t		
7. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT		
	*	
Attachment(s)		
1. Notice of References Cited (PTO-892)	5. Notice of Ir	formal Patent Application (PTO-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. Interview S	ummary (PTO-413), /Mail Date
3. Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date	08), 7. ☐ Examiner's	Amendment/Comment
Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. ⊠ Examiner's 9. ☐ Other	Statement of Reasons for Allowance
		JEROME OFFINT II PRIMARY EXAMINER

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Reasons for Allowance

1.

Claims 1-3 are allowed for the reason the prior art does not teach or suggest in claimed combination, "... the output signal generated in said bit conversion step is read and output as an address signal, so that a reotation and/or reversal process to a former image is performed."

Claims 4-6 are allowed for the reason the prior art does not teach or suggest in claimed combination, "... output signal generated by said bit conversion means is read and output as an address signal, so that the rotation and or reversal process to a former image is performed."

Claim 7 is allowed for the reason the prior art does not teach or suggest in claimed combination, "... and the image data written in the memory is read according to addresses of predetermined order, so that a rotation and/or reversal process to a former image is performed."



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Claim 8 is allowed for the reason the prior art does not teach in claimed combination, "... and the image data written in said storage means is read according to addresses of predetermined order, so that a rotation and /or reversal process to a former image is performed."

Claim 9 is allowed for the reason the prior art does not teach or suggest in claimed combination, "... the output signal generated in said bit conversion module is read and output as an address signal, so that a rotation and /or reversal rotation/reversal process to a former image is performed."

Claim 10 is allowed for the reason the prior art does not teach or suggest in claimed combination, "... and by using the output signal generated in said bit conversion module as an address signal, and the image data written in the memory is read according to addresses of predetermined order, so that a rotation and /or reversal process to a former image is performed."

Claim 11 is allowed for the reason the prior art does not teach or suggest in claimed combination, "... the position information of the entire image before the rotation of the reversal where the block image stands is converted into the positional information for the entire image after the image rotation or the image reversal in said image rotation and or reversal processing step, and the

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converted positional information is added to the rotated or reversed block image and output."

Claim 12 is allowed for the reason the prior art does not teach or suggest in claimed combination, "... the positional information for the entire image before the rotation or the reversal where the image rotation or the image reversal by said image rotation and or reversal processing means, and the converted positional information is added to the rotated or reversed block image and output."

Claim 13 is allowed for the reason the prior art does not teach or suggest in claimed combination, "... the positional information for the entire image before the rotation or the reversal where the block image stands is converted into the positional information for the entire image after the image rotation or the image reversal in said image rotation and or reversal processing module, and the converted positional information is added to the rotated or reversed block image and output."

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jerome Grant II whose telephone number is 571-272-7463. The examiner can normally be reached on Mon.-Thurs. from 9:00 to 5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kimberly A. Williams, can be reached on 571-272-7471. The fax phone number for the organization where this application or proceeding is assigned is 703-372-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

J Grant II JEROME GRANT II PRIMARY EXAMINER